



# MIC45205 Evaluation Board

## 26V/6A DC-to-DC Power Module

### General Description

Micrel's MIC45205 is a synchronous step-down regulator module, featuring a unique adaptive ON-time control architecture. The module incorporates a DC-to-DC controller, power MOSFETs, bootstrap diode, bootstrap capacitor, and an inductor in a single package; simplifying the design and layout process for the end user.

This highly integrated solution expedites system design and improves product time-to-market. The internal MOSFETs and inductor are optimized to achieve high efficiency at a low output voltage. The fully optimized design can deliver up to 6A current under a wide input voltage range of 4.5V to 26V, without requiring additional cooling.

The MIC45205-1 uses Micrel's HyperLight Load<sup>®</sup> (HLL) MIC45205-2 uses Micrel's Hyper Speed Control<sup>™</sup> architecture which enables ultra-fast load transient response, allowing for a reduction of output capacitance. The MIC45205 offers 1% output accuracy that can be adjusted from 0.8V to 5.5V with two external resistors.

The basic parameters of the evaluation board are:

- Input: 4.5V to 26V
- Output 0.8V to 5V at 6A
- 600kHz Switching Frequency
  - Adjustable 200kHz to 600kHz

Datasheets and support documentation are available on Micrel's web site at: [www.micrel.com](http://www.micrel.com).

### Requirements

The MIC45205-1 and MIC45205-2 evaluation board requires only a single power supply with at least 10A current capability. No external linear regulator is required to power the internal biasing of the IC because the MIC45205 has an internal PVDD LDO. In the applications with  $V_{IN} < +5.5V$ , PVDD should be tied to VIN to bypass the internal linear regulator. The output load can either be a passive or an active load.

### Precautions

The MIC45205 evaluation board does not have reverse polarity protection. Applying a negative voltage to the VIN and GND terminals may damage the device. The maximum VIN of the board is rated at 26V. Exceeding 30V on the VIN could damage the device.

### Getting Started

#### 1. VIN Supply

Connect a supply to the VIN and GND terminals, paying careful attention to the polarity and the supply range ( $4.5V < V_{IN} < 26V$ ). Monitor IIN with a current meter and monitor input voltage at VIN and GND terminals with a voltmeter. Do not apply power until Step 4.

#### 2. Connect Load and Monitor Output

Connect a load to the VOUT and GND terminals. The load can be either a passive (resistive) or an active (as in an electronic load) type. A current meter may be placed between the VOUT terminal and load to monitor the output current. Ensure the output voltage is monitored at the VOUT terminal.

#### 3. Enable Input

The EN pin has an on board 100kΩ pull-up resistor (R10) to VIN, which allows the output to be turned on when PVDD exceeds its UVLO threshold. An EN connector is provided on the evaluation board for users to easily access the enable feature. Applying an external logic signal on the EN pin to pull it low or using a jumper to short the EN pin to GND will shut off the output of the MIC45205 evaluation board.

#### 4. Turn Power

Turn on the VIN supply and verify that the output voltage is regulated to 5V.

### Ordering Information

| Part Number      | Description                 |
|------------------|-----------------------------|
| MIC45205-1YMP EV | MIC45205-1 Evaluation Board |
| MIC45205-2YMP EV | MIC45205-2 Evaluation Board |

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## Features

### Feedback Resistors

The output voltage on the MIC45205 evaluation board, which is preset to 5.0V, is determined by the feedback divider, as illustrated in Equation 1:

$$V_{OUT} = V_{REF} \times \left( 1 + \frac{R_{14}}{R_{BOTTOM}} \right) \quad \text{Eq. 1}$$

where  $V_{REF} = 0.8V$ , and  $R_{BOTTOM}$  is one of R3 thru R9. Leaving the  $R_{BOTTOM}$  open by removing all jumpers on the feedback headers gives a 0.8V output voltage. All other voltages not listed above can be set by modifying  $R_{BOTTOM}$  value according to Equation 2:

$$R_{BOTTOM} = \frac{R_{14} \times V_{REF}}{V_{OUT} - V_{REF}} \quad \text{Eq. 2}$$

Note that the output voltage should not be set to exceed 5V.

**Table 1. Typical Values of Some Components**

| $V_{OUT}$ | $V_{IN}$  | <b>R14</b><br>(Top Feedback Resistor) | <b>R</b><br>(Bottom Feedback Resistor) | <b>C14</b><br>(C#) | $C_{OUT}$        |
|-----------|-----------|---------------------------------------|----------------------------------------|--------------------|------------------|
| 1.0V      | 5V to 26V | 10k $\Omega$                          | 40.2k $\Omega$                         | 2.2nF              | 100 $\mu$ F/6.3V |
| 1.2V      | 5V to 26V | 10k $\Omega$                          | 20.0k $\Omega$                         | 2.2nF              | 100 $\mu$ F/6.3V |
| 1.5V      | 5V to 26V | 10k $\Omega$                          | 11.5k $\Omega$                         | 2.2nF              | 100 $\mu$ F/6.3V |
| 1.8V      | 5V to 26V | 10k $\Omega$                          | 8.06k $\Omega$                         | 2.2nF              | 100 $\mu$ F/6.3V |
| 2.5V      | 5V to 26V | 10k $\Omega$                          | 4.75k $\Omega$                         | 2.2nF              | 100 $\mu$ F/6.3V |
| 3.3V      | 5V to 26V | 10k $\Omega$                          | 3.24k $\Omega$                         | 2.2nF              | 100 $\mu$ F/6.3V |
| 5V        | 7V to 26V | 10k $\Omega$                          | 1.91k $\Omega$                         | 2.2nF              | 100 $\mu$ F/6.3V |

## SW Node

A test pad is placed for monitoring the switching waveform, which is one of the most critical waveforms for the converter.

## Current Limit

The MIC45205 uses the  $R_{DS(ON)}$  of the low-side MOSFET and external resistor connected from the ILIM pin to the SW node to decide the current limit.

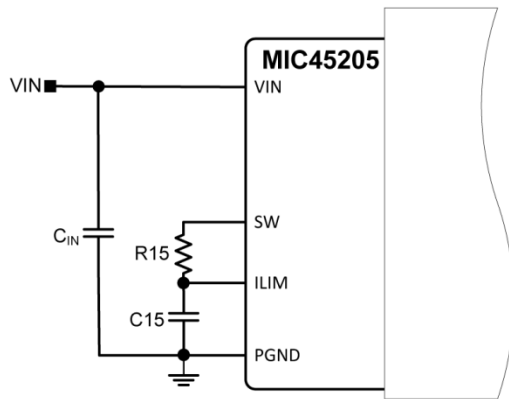


Figure 1. MIC45205 Current-Limiting Circuit

In each switching cycle of the MIC45205, the inductor current is sensed by monitoring the low-side MOSFET in the OFF period. The sensed voltage  $V_{(ILIM)}$  is compared with the power ground (PGND) after a blanking time of 150ns. In this way the drop voltage over the resistor R15 ( $V_{CL}$ ) is compared with the drop over the bottom FET generating the short current limit. The small capacitor (C15) connected from ILIM pin to PGND filters the switching node ringing during the off-time allowing a better short-limit measurement. The time constant created by R15 and C15 should be much less than the minimum off time.

The  $V_{CL}$  drop allows programming of short limit through the value of the resistor (R15) if the absolute value of the voltage drop on the bottom FET is greater than  $V_{CL}$ . In that case the  $V_{(ILIM)}$  is lower than PGND and a short circuit event is triggered. A hiccup cycle to treat the short event is generated. The hiccup sequence including the soft-start reduces the stress on the switching FETs and protects the load and supply for severe short conditions.

The short-circuit current limit can be programmed by using Equation 3:

$$R15 = \frac{(I_{CLIM} - \Delta I_{L(PP)} \times 0.5) \times R_{DS(ON)} + V_{CL}}{I_{CL}} \quad \text{Eq. 3}$$

where:

$I_{CLIM}$  = Desired current limit

$R_{DS(ON)}$  = On-resistance of low-side power MOSFET, 16mΩ typically

$V_{CL}$  = Current-limit threshold (typical absolute value is 14mV per Electrical Characteristics in the MIC45205 data sheet)

$I_{CL}$  = Current-limit source current (typical value is 80μA, per Electrical Characteristics in the MIC45205 data sheet).

$\Delta I_{L(PP)}$  = Inductor current peak-to-peak, since the inductor is integrated, use Equation 4 to calculate the inductor ripple current.

The peak-to-peak inductor current ripple is:

$$\Delta I_{L(PP)} = \frac{V_{OUT} \times (V_{IN(MAX)} - V_{OUT})}{V_{IN(MAX)} \times f_{SW} \times L} \quad \text{Eq. 4}$$

The MIC45205 has 1.0μH inductor integrated into the module. In case of hard short, the short limit is folded down to allow an indefinite hard short on the output without any destructive effect. It is mandatory to make sure that the inductor current used to charge the output capacitance during soft start is under the folded short limit; otherwise the supply will go in hiccup mode and may not be finishing the soft start successfully.

The MOSFET  $R_{DS(ON)}$  varies 30 to 40% with temperature. Therefore, it is recommended to add a 50% margin to  $I_{CLIM}$  in the above equation to avoid false current limiting due to increased MOSFET junction temperature rise. With  $R15 = 1.37k\Omega$  and  $C15 = 15pF$ , the typical output current limit is 8A.

### Setting the Switching Frequency

The MIC45205 switching frequency can be adjusted by changing the value of resistors R1 and R2. The switching frequency also depends on VIN, VOUT and load conditions.

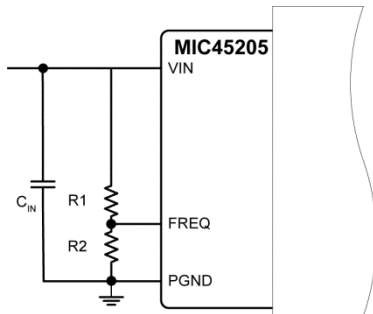


Figure 2. Switching Frequency Adjustment

Equation 5 gives the estimated switching frequency:

$$f_{sw} = f_o \times \frac{R2}{R1 + R2} \quad \text{Eq. 5}$$

where:

$$f_o = 600\text{kHz}$$

$$R1 = 100\text{k}\Omega \text{ (recommended)}$$

R2 is selected to set the required switching frequency as shown in Figure 3:

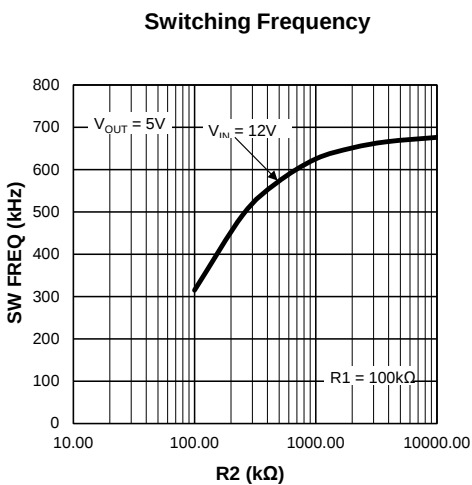


Figure 3. Switching Frequency vs. R2

**Figure 4. Schematic of MIC45205 Evaluation Board**

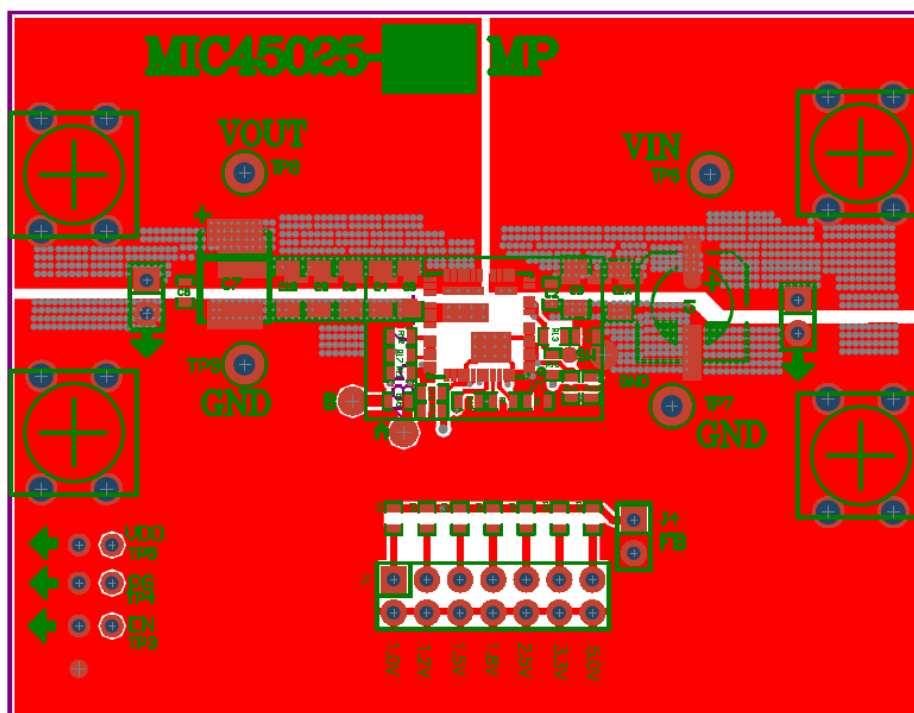
## Bill of Materials

| Item                         | Part Number         | Manufacturer                | Description                              | Qty. |
|------------------------------|---------------------|-----------------------------|------------------------------------------|------|
| C1                           | B41125A7227M        | TDK <sup>(1)</sup>          | 220μF/35V, ALE Capacitor (optional)      | 1    |
| C1X, C6, C9,<br>C10, C7, C13 |                     |                             | Open                                     | 6    |
| C3                           | C3216X5R1H106M160AB | TDK                         | 10uF/50V, 1206, X5R, 10%, MLCC           | 1    |
| C2, C4, C8                   | GRM188R71H104KA93D  | Murata <sup>(2)</sup>       | 0.1μF/50V, X7R, 0603, 10%, MLCC          | 3    |
| C5                           | C3216X5R0J107M160AB | TDK                         | 100μF/6.3V, X5R, 1206, 20%, MLCC         | 1    |
| C12                          | C1608C0G1H222JT     | TDK                         | 2.2nF/50V, NP0, 0603, 5%, MLCC           | 1    |
| C11                          | GRM1885C1H150JA01D  | Murata                      | 15pF/50V, NP0, 0603, 5%, MLCC            | 3    |
| CON1, CON2,<br>CON3, CON4    | 8174                | Keystone <sup>(3)</sup>     | 15A, 4-Prong Through-Hole Screw Terminal | 4    |
| J1                           | M50-3500742         | Harwin <sup>(4)</sup>       | Header 2x7                               | 1    |
| J2, J3, J4,<br>TP3 – TP5     | 90120-0122          | Molex <sup>(5)</sup>        | Header 2                                 | 6    |
| JPx1, JPx2                   |                     |                             | Open                                     | 2    |
| R1, R10                      | CRCW0603100K0FKEA   | Vishay Dale <sup>(6)</sup>  | 100kΩ, 1%, 1/10W, 0603, Thick Film       | 2    |
| R2, R12,<br>R13, R16         |                     |                             | Open                                     | 4    |
| R3                           | CRCW060340K2FKEA    | Vishay Dale                 | 40.2kΩ, 1%, 1/10W, 0603, Thick Film      | 1    |
| R4                           | CRCW06020K0FKEA     | Vishay Dale                 | 20kΩ, 1%, 1/10W, 0603, Thick Film        | 1    |
| R5                           | CRCW060311K5FKEA    | Vishay Dale                 | 11.5kΩ, 1%, 1/10W, 0603, Thick Film      | 1    |
| R6                           | CRCW06038K06FKEA    | Vishay Dale                 | 8.06kΩ, 1%, 1/10W, 0603, Thick Film      | 1    |
| R7                           | CRCW06034K75FKEA    | Vishay Dale                 | 4.75kΩ, 1%, 1/10W, 0603, Thick Film      | 1    |
| R8                           | CRCW06033K24FKEA    | Vishay Dale                 | 3.24kΩ, 1%, 1/10W, 0603, Thick Film      | 1    |
| R9                           | CRCW06031K91FKEA    | Vishay Dale                 | 1.91kΩ, 1%, 1/10W, 0603, Thick Film      | 1    |
| R11                          | CRCW060349K9FKEA    | Vishay Dale                 | 49.9kΩ, 1%, 1/10W, 0603, Thick Film      | 1    |
| R14                          | CRCW060310K0FKEA    | Vishay Dale                 | 10kΩ, 1%, 1/10W, 0603, Thick Film        | 1    |
| R15                          | CRCW06031K37FKEA    | Vishay Dale                 | 1.37kΩ, 1%, 1/10W, 0603, Thick Film      | 1    |
| R17, R18, R19                | RCG06030000Z0EA     | Vishay Dale                 | 0Ω Resistor, 1%, 1/10W, 0603, Thick Film | 3    |
| TP6 – TP9,<br>JPx3, JPx4     | 1502-2              | Keystone                    | Single-End, Through-Hole Terminal        | 6    |
| U1                           | MIC45205-1YMP       | Micrel, Inc. <sup>(7)</sup> | 26V/6A DC-to-DC Power Module             | 1    |
|                              | MIC45205-2YMP       |                             |                                          |      |

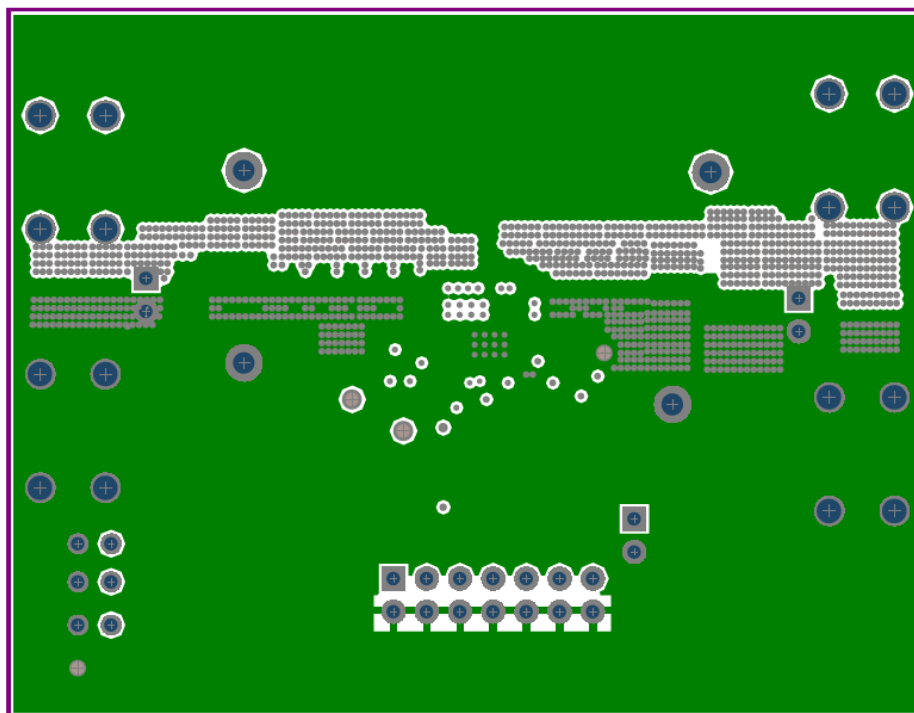
### Notes:

1. TDK: [www.TDK.com](http://www.tdk.com).
2. Murata: [www.murata.com](http://www.murata.com).
3. Keystone: [www.keyelco.com](http://www.keyelco.com).
4. Harwin: <http://www.harwin.com>
5. Molex: [www.molex.com](http://www.molex.com).
6. Vishay-Dale: [www.vishay.com](http://www.vishay.com).
7. Micrel: [www.micrel.com](http://www.micrel.com).

## PCB Layout Recommendations

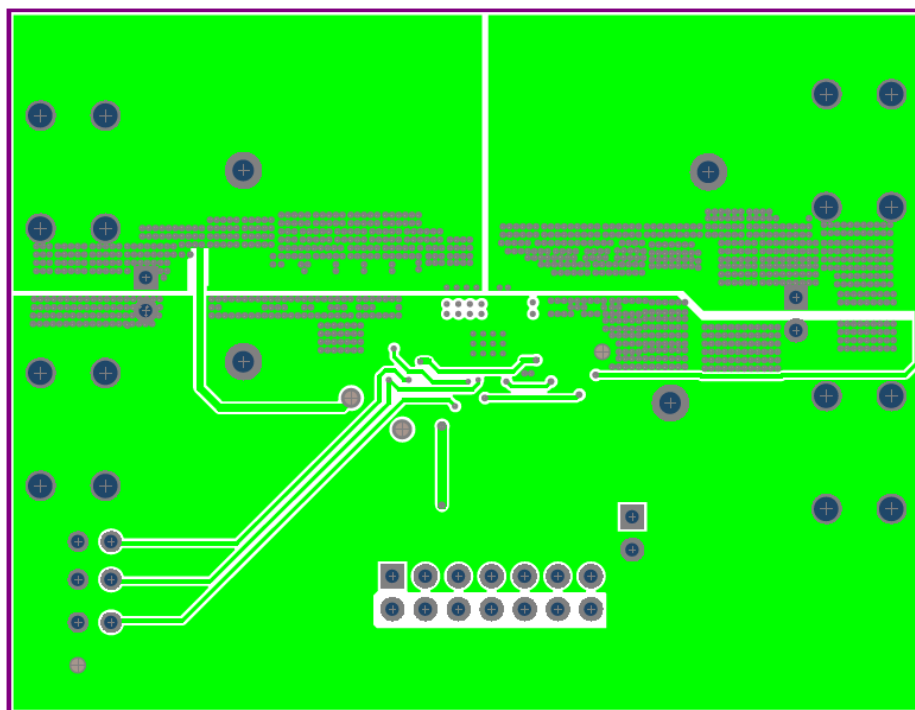


MIC45205 Evaluation Board Top Layer

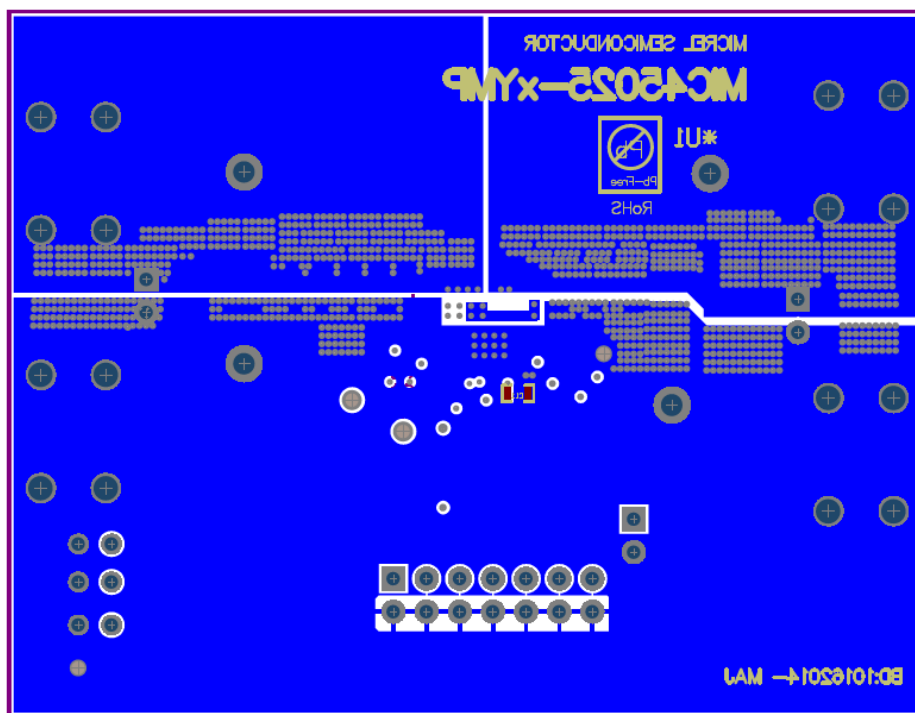


MIC45205 Evaluation Board Copper Layer 2

## PCB Layout Recommendations (Continued)



MIC45205 Evaluation Board Copper Layer 3



MIC45205 Evaluation Board Bottom Layer

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